



Figure 7-1
65C02 timing signals

Table 7-5
65C02 timing signal descriptions

Signal	Description
14M	Master oscillator, 14.318 MHz; also 80-column dot clock
VID7M	Intermediate timing signal and 40-column dot clock
Q3	Intermediate timing signal, 2.045 MHz with asymmetrical duty cycle
φ0	Phase 0 of 65C02 clock, 1.0227 MHz; complement of φ1